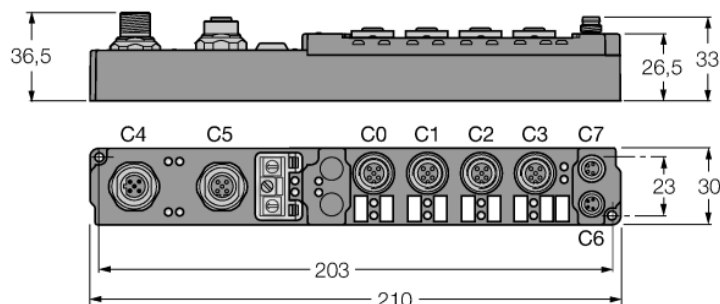
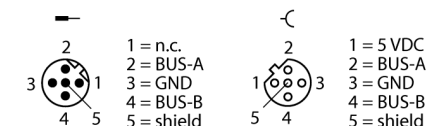


# 用于PROFIBUS-DP的piconet独立模块 4通道模拟量输入0(4)... 20 mA SDPB-40A-1007

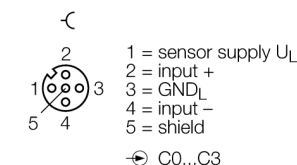


- 4通道模拟量输入0(4)... 20 mA
- Configuration interface
- Parametrizable functions
- Supported via I/O-ASSISTANT 2
- 直接连接至现场总线
- 玻璃光纤加固外壳
- 冲击和振动测试
- 模块电路封装
- 金属接插件
- 防护等级IP67

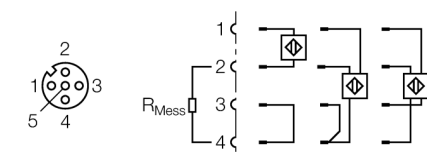
## M12 × 1 现场总线



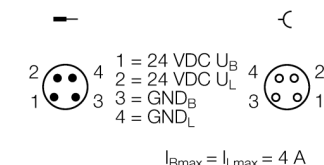
## M12 × 1 输入



## 输入接线



## M8 × 1 电源



|                          |                                 |
|--------------------------|---------------------------------|
| 型号                       | SDPB-40A-1007                   |
| 货号                       | 6824439                         |
| 通道数                      | 4                               |
| 工作/负载电压                  | 20...29 VDC                     |
| 工作电流                     | ≤ 55 mA                         |
| 现场总线传输速率                 | 9.6 kbps ... 12 Mbps            |
| 现场总线地址设定                 | 0 到 99                          |
| 服务接口                     | 使用I/O-ASSISTANT设定参数             |
| 电气隔离                     | 现场总线与工作电压之间                     |
| 通道数                      | 4 analogue inputs 20 mA         |
| 输入阻抗                     | 80 Ω                            |
| 电气隔离                     | channels to operational voltage |
| Common mode voltage      | max. 35 V                       |
| 电流测量                     | 0.5 mA                          |
| Conversion time          | 250 ms                          |
| Relative measuring error | < ±0.3 % ( 全量程 )                |
| Input filter             | variable                        |
| 传感器供电                    | from load voltage               |
| 尺寸 (长/宽/高)               | 30 x 210 x 26.5 mm              |
| 振动测试                     | 符合EN 60068-2-6标准                |
| 冲击测试                     | 根据 DIN EN 60068-2-27            |
| 电磁兼容性                    | 符合EN 61000-6-2/EN 61000-6-4标准   |
| 防护等级                     | IP67                            |
| 认证                       | CE, cULus                       |

过程映像中的数据

Valid for the setting "Motorola format"

SBn: Status byte channel n  
CBn: Control byte channel n  
Chn D0: channel n,  
least significant data byte  
Chn D1: channel n,  
most significant data byte

|                                                                                                                                                                                                                                           | Address | Input data |          | Output data |          |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|------------|----------|-------------|----------|
| Pre-conditions                                                                                                                                                                                                                            | Word    | High-Byte  | Low-Byte | High-Byte   | Low-Byte |
| <b>Compact mapping:</b><br>Starting with Ch0 D1 in "Low-Byte" word 0 all other bytes follow immediately. Only the user data are mapped (greyed in the table).<br><b>Complex mapping:</b><br>Data are mapped with control and status byte. | 0       | Ch0 D1     | SB0      | Ch0 D1      | CB0      |
|                                                                                                                                                                                                                                           | 1       | SB1        | Ch0 D0   | CB1         | Ch0 D0   |
|                                                                                                                                                                                                                                           | 2       | Ch1 D0     | Ch1 D1   | Ch1 D0      | Ch1 D1   |
|                                                                                                                                                                                                                                           | 3       | Ch2 D1     | SB2      | Ch2 D1      | CB2      |
|                                                                                                                                                                                                                                           | 4       | SB3        | Ch2 D0   | CB3         | Ch2 D0   |
|                                                                                                                                                                                                                                           | 5       | Ch3 D0     | Ch3 D1   | Ch3 D0      | Ch3 D1   |